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[R Ghiya](#)

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[Is it a tree, a DAG, or a cyclic graph? A shape analysis for **heap**-directed pointers in C - all 10 versions »](#)

R Ghiya, LJ Hendren - Proceedings of the 23rd ACM SIGPLAN-SIGACT symposium on ..., 1996 - portal.acm.org

... primarily use recursive data structures, or a **combination** of ar ... rules assuming a simple model where **stack**-directed pointers and **heap**-directed pointers ...

Cited by 171 - Related Articles - Web Search - BL Direct

[Buffer overflows: attacks and defenses for the vulnerability of the decade - all 96 versions »](#)

C Cowan, P Wagle, C Pu, S Beattie, J Walpole - Foundations of Intrusion Tolerant Systems, 2003 [Organically ..., 2003 - ieeexplore.ieee.org

... compatibility or performance [9]. Section 4 discusses which **combinations** of defenses ... Function pointers can be allo- cated anywhere (**stack**, **heap**, static data ...

Cited by 170 - Related Articles - Web Search

[Stack-based typed assembly language - all 36 versions »](#)

G MORRISETT, K CRARY, N GLEW, D WALKER - Journal of Functional Programming, 2002 - Cambridge Univ Press

... passing style (CPS) transform, which eliminated the need for a control **stack**. In particular, activation records were represented by **heap**-allocated closures as ...

Cited by 139 - Related Articles - Web Search - Library Search - BL Direct

[Visualizing principles of abstract machines by generating interactive animations - all 12 versions »](#)

S Diehl, T Kunze - FUTURE GENER COMPUT SYST, 2000 - rw4.cs.uni-sb.de

... In **combination** with the GANIMAM base package classes these class files form an interactive Java ... Clicking at a cell of a **stack**, **heap** or register opens a window. ...

Cited by 16 - Related Articles - View as HTML - Web Search - Library Search

[Beyond **stack** smashing: recent advances in exploiting buffer overruns - all 4 versions »](#)

J Pincus, B Baker - Security & Privacy Magazine, IEEE, 2004 - ieeexplore.ieee.org

... example is in terms of a **stack** buffer overrun ... modification on overruns em- bedded in **heap** structs or ... of data-pointer modification is in **combination** with function ...

Cited by 40 - Related Articles - Web Search - BL Direct

[\[book\] An introduction to object-oriented programming - all 8 versions »](#)

T Budd - 1991 - engr.oregonstate.edu

Page 1. An Introduction to Object-Oriented Programming (2nd Ed) Timothy A. Budd Oregon State University Corvallis, Oregon USA All rights reserved. ...

Cited by 266 - Related Articles - View as HTML - Web Search

[Predictable Space Behaviour in FSM-Hume - all 5 versions »](#)

K Hammond, GJ Michaelson - Proc. Implementation of Functional Langs.(IFL'02), Madrid, ...

- Springer

... It is unusual in being based on a **combination** of λ -calculus and finite state ... This paper describes a simple model for calculating **stack** and **heap** costs in ...

Cited by 14 - Related Articles - Web Search - BL Direct

Address obfuscation: An efficient approach to combat a broad range of memory error exploits - all 13 versions »

S Bhatkar, DC DuVarney, R Sekar - Proceedings of the 12th USENIX Security Symposium, 2003 - andrew.cmu.edu

... the **stack**), function pointers (stored on the **stack**, static area or the **heap**), the global ... operating systems, there are three possible **combinations** of goals ...

Cited by 118 - Related Articles - View as HTML - Web Search

Checkpointing Message-Passing Interface (MPI) parallel programs - all 5 versions »

WJ Li, JJ Tsay - Fault-Tolerant Systems, 1997. Proceedings., Pacific Rim ..., 1997 - ieeexplore.ieee.org

... Our implementation is a **combination** of coordinated and uncoordinated checkpointing. ... a program consists of text segment, data segment, **stack** and **heap**. ...

Cited by 12 - Related Articles - Web Search

ORBIT: an optimizing compiler for scheme - all 3 versions »

D Kranz, R Kelsey, J Rees, P Hudak, J Philbin - Proceedings of the 1986 SIGPLAN symposium on Compiler ..., 1986 - portal.acm.org

... these benchmarks, Orbit currently produces code that is competitive with and in many cases superior to comparable compiler/machine **combinations**. ...

Cited by 264 - Related Articles - Web Search - Library Search - BL Direct

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Result # 1 Relevance:

Linked List Stack

2006-12-17 IPCOM000144069D English

Disclosed is a method for reducing address space consumption for a thread's stack with the total stack size. The stack for a thread is divided into multiple discontiguous segments which may hold many stack frames), which are allocated as needed (manual ...

Result # 2 Relevance:

(RSS) Memory-usage sensitive control of Just-in-time compiler

2006-02-10 IPCOM000133828D English

Disclosed is an invention for the Java interpreter and JIT (just-in-time) compiler to use about memory usage when selecting methods to be compiled.

Result # 3 Relevance:

Freeing Thread Stacks in OS/2 using the ALPs Begin and End Thread API

1992-11-01 IPCOM000110302D English

This article is related to the IBM Automated Logistics and Production Solutions (ALPS) component. Disclosed is the ALPS Thread Begin and End APIs.

Result # 4 Relevance:

Method for hardware stack-based architecture for processor pipelines in threaded systems

21-Sep-2005 IPCOM000128937D English

Disclosed is a method for hardware stack based architecture for processor pipelines in threaded systems. Benefits include improved functionality, improved performance, and improved

Result # 5 Relevance:

Main Memory Data Object Retention

1983-07-01 IPCOM000046515D English

This invention is directed to a CPU implementable method for managing main memory environment so as to retain a data object in main memory past the lifetime of the process. The method steps include (1) ascertaining whether an object can be ...

Result # 6 Relevance:

Dynamic Stack Management in Multi-tasking Multi-threaded Operating Systems

1991-09-01 IPCOM000121706D English

This article describes the architecture for operating system (OS)- provided dynamic stack management that allows stack pages to be allocated and committed on demand by the

Result # 7 Relevance:

Method for creating balanced memory pools for LPAR creation

2005-11-21 IPCOM000131837D English

Describes a method for creating memory pools of equal size when an LPAR is started.

Result # 8 Relevance:

Allocating Heap and Stack Memory

1991-10-01

IPCOM000122041D

English

This invention relates to a computer-implemented method for allocating heap and stack memory during runtime execution of compiled code. The method steps comprise: (a) the allocation of storage under the stack manager; and (b) releasing the allocation ...

Result # 9 Relevance:

Sharing a Single Stack Among Multiple Concurrent Threads

1993-10-01

IPCOM000106222D

English

Disclosed is a method that allows multiple threads running within an application to share a physical stack. New STACK and UNSTACK instructions are defined that allow the stack of different threads to be interleaved within the same memory block (or stack). ...

Result # 10 Relevance:

Operating System/2 Memory Management Extension

1991-02-01

IPCOM000119677D

English

A program is disclosed that deallocates a stack for threads, only after assuring that the threads have exited.

Displaying page 1 of 50 [<< FIRST](#) | [< BACK](#) | [NEXT >](#) | [LAST >>](#)

Search A method and system in accordance with the present invention comprises a thread specific heap combination, wherein the thread heap is for thread local memory usage. The thread stack and thread heap grow in opposite directions. In the present invention, the thread specific heap is allocated next to the thread's stack and grows in the opposite direction of the stack. This improvement allows the current space management of threads to spread out the memory placement of multiple stacks to avoid collision, to also use multiple thread specific heaps without additional overhead or complexity. It also allows the existing growing of the stack by adding memory pages to the process for the stack to be used again because the stack grows in the opposite direction. Thread specific heaps eliminate the need for thread synchronization when allocating from a shared heap in a multiprocessor environment. In the present invention, a method and system is provided that reduces contention for private memory and does not increase the number of memory regions needed. The thread stack and a thread heap are combined for memory allocation into a single region with a dead zone separating it from other thread stack/heap combinations.

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